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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/700,902	11/04/2003	Michael Zhuoying Su	1001-0263 7761		
22120 7	590 02/23/2006		EXAMINER		
ZAGORIN O'BRIEN GRAHAM LLP 7600B N. CAPITAL OF TEXAS HWY.			DIMYAN, MAGID Y		
SUITE 350		••	ART UNIT	PAPER NUMBER	
AUSTIN, TX	78731		2825		

DATE MAILED: 02/23/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	3			
	10/700,902	SU, MICHAEL ZHUOYING				
Office Action Summary	Examiner	Art Unit				
	Magid Y. Dimyan	2825				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 04 N	lovember 2003.					
2a) This action is FINAL . 2b) ⊠ This	s action is non-final.					
3) Since this application is in condition for allowa	nce except for formal matters, p	rosecution as to the merits is				
closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11,	453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1-46</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)☐ Claim(s) is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) <u>1-46</u> are subject to restriction and/or	election requirement.					
Application Papers						
9)☐ The specification is objected to by the Examine	er.					
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the	drawing(s) be held in abeyance. S	ee 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Ex	caminer. Note the attached Office	e Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
1.☐ Certified copies of the priority document	s have been received.					
2. Certified copies of the priority document	s have been received in Applica	tion No				
3. ☐ Copies of the certified copies of the prio	*	ved in this National Stage				
application from the International Burea						
* See the attached detailed Office action for a list	of the certified copies not receive	/ea.				
Attachment(s)	"	(DTA 110)				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summa Paper No(s)/Mail					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) Notice of Informal	Patent Application (PTO-152)				
Paper No(s)/Mail Date	6) Other:					

Application/Control Number: 10/700,902

Art Unit: 2825

DETAILED ACTION

Page 2

Election/Restrictions

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1 19, drawn to an integrated circuit comprising a speed sensing circuit and a capacitive load selectively coupled to the speed sensing circuit formed by two metal layers separated by an insulating layer, classified in class 257, subclass 734.
 - II. Claims 20 27 and 45 46, drawn to a method and apparatus comprising coupling a capacitive load to a speed sensing circuit, selectively configuring the capacitive load, and measuring the delay corresponding to the capacitive load, classified in class 716, subclass 6.
 - III. Claims 28 44, drawn to a method of manufacturing an integrated circuit comprising forming a speed sensing circuit and forming a capacitive load selectively coupled to the speed sensing circuit formed by two metal layers separated by an insulating layer, classified in class 438, subclass 584.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions Group I (claims 1 – 19), Group II (claims 20 – 27 and 45 – 46) and Group III (claims 28 – 44) are directed to related to an integrated circuit with a speed sensing circuit and a capacitive load. The related inventions are distinct if the inventions as claimed do not overlap in scope, i.e., are mutually exclusive; the inventions as claimed are not obvious variants; and the inventions as claimed are either not capable

of use together or can have a materially different design, mode of operation, function, or effect. See MPEP § 806.05(j). In the instant case, Group I is related to a speed sensing circuit and a capacitive load selectively coupled to the speed sensing circuit, formed by two metal layers separated by an insulating layer; Group II is related to a method and apparatus that include coupling a capacitive load to a speed sensing circuit, selectively configuring the capacitive load, and measuring a delay corresponding to the capacitive load; and Group III is related to a method of manufacturing an IC comprising forming a speed sensing circuit, and forming a capacitive load selectively coupled to the speed sensing circuit, the capacitive load formed by two metal layers separated by an insulating layer.

3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

4. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more currently named inventors is no longer an inventor of at least one claim

Art Unit: 2825

remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Magid Y. Dimyan whose telephone number is (571) 272-1889. The examiner can normally be reached on Monday - Friday 8:00 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on (571) 272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Magid Y Dimyan Examiner Art Unit 2825

myd 13 February 2006

PRIMARY EXAMINER